		STUDY MODULE D	ESCRIPTION FORM				
Name o	f the module/subject	alactrotachnology	Code				
Field of	study	electrotechnology	Profile of study	Year /Semester			
Dow	er Engineering		(general academic, practical)	4.1.4			
Flective			Subject offered in:	Course (compulsory elective)			
LICCIVE	pathopecialty	-	polish	obligatory			
Cycle o	f study:		Form of study (full-time,part-time)				
	Second-c	ycle studies	part-time				
No. of h	ours			No. of credits			
Lectu	re: 10 Classes	s: 10 Laboratory: -	Project/seminars:	- 3			
Status of	of the course in the study	program (Basic, major, other)	(university-wide, from another field)				
		(brak)	()	brak)			
Educati	on areas and fields of sci	ence and art		ECTS distribution (number and %)			
techr	nical sciences			3 100%			
	Technical scie	ences		3 100%			
Resp	onsible for subi	ect / lecturer:					
Pro ema tel. Elel ul. F	f. dr hab. inż. Konrad 3 ail: konrad.skowronek 616652388 ktryczny Piotrowo 3A, 60-965 P	Skowronek @put.poznan.pl oznań					
Prere	equisites in term	s of knowledge, skills an	d social competencies:				
1	Knowledge	Knowledge of mathematics, phy	rsics and circuit theory at the leve	el of the first degree.			
2	Skills	Enhanced ability to understand in a field related to the chosen fi	and interpret the messages convelot of study.	veyed and effective self-study			
3	Social competencies	It has an expanded awareness of cooperate and work independer	of the need to broaden their com tly and within a team.	petence, willingness to			
Assu	mptions and obj	ectives of the course:					
Unders the prin Unders circuits	standing the theory of nciples of passive two standing the dynamic s s, in particular the actu	discrete circuits. Understanding th -way adapter synthesis. Knowledg analysis of electrical circuits. In-de ial (random).	ne principles of Fourier and Lapla ge of topological methods for ana opth knowledge of analytical met	ace transforms. Familiar with alysis of electrical circuits. hods for calculating electrical			
	Study outco	mes and reference to the	educational results for a	a field of study			
Knov	viedge:						
1. Cha dynam	racterized discrete cire ics and stability of the	cuits, circuits and real signals, des electrical circuits and their synthe	scribe and explain the laws and r sis [K_W01++, K_W03+++]	nethods of analysis of the			
2. Rec	ognize and select app	ropriate methods of in-depth analy	ysis of electrical circuits [K_W0)2++]			
Skills	6:						
1. Use and ra	in-depth knowledge o ndom) circuitry, such a	f the theory of electrical circuits ne as indicators of stability, capacity a	ecessary to determine the param and random factors, transfer func	eters of the actual (discrete ctions, etc [K_U03 ++]			
2. Obta solve p	ain specific information problems in the field of	n trom the literature and the Intern	et, work individually and collectivits [K_U01 ++]	vely, individually and collectively			
3. To u	use methods get to kno	ow and mathematical models to th	ne purpose of analysis and the ar	rangements and power			
Socia	al competencies:						
1. Able	to think and act in an	entrepreneurial manner in the ex	tended analysis of electrical circu	uits [K_K01 +, K_K02 +]			
_				-			
Assessment methods of study outcomes							

Lecture:

? assess the knowledge and skills listed on the written and oral exam with in-depth theory of electrical circuits.

Tutorials:

? to evaluate solving skills extended accounting tasks for the analysis of electrical circuits - checking skills in each class, and two colloquia during the semester.

Get extra points for the activity in the classroom, and in particular for:

? propose to discuss additional aspects of the subject;

- ? the effectiveness of the application of the knowledge gained during solving the given problem;
- ? subsequent to the improvement of teaching materials;

? developed aesthetic care tasks - in the self-study.

Course description

The theory of discrete time circuits and what to value. Principles of Fourier transforms to choose between and Laplace. Synthesis methods and schemes passive two-way adapter. Some problems of topology of electrical circuits. Issues dynamics of electrical circuits and their evaluation. Analytical method for calculating random electrical circuits.

Basic bibliography:

1. Bolkowski S. "Teoria Obwodów elektrycznych", WNT. Warszawa 1998

2. Szabatin J., Śliwa E. "Zbiór zadań z teorii obwodów. Część 1", Wyd. Pol. Warsz. Warszawa 1997

3. Skowronek K. "Obwody elektryczne w ujęciu stochastycznym", Wyd. PP, 2011

4. Mikołajuk K., Trzaska Z. "Zbiór zadań z elektrotechniki teoretycznej", WNT, W-a, 1978

Additional bibliography:

1. Krakowski M. "Elektrotechnika teoretyczna", PWN, Warszawa 1978.

2. Chua L.O., Desoer C.A., Kuh E.S. " Linear and Nonlinear Circuits", McGraw-Hill Inc., 1987

3. Jastrzębska G., Nawrowski R. " Zbiór zadań z podstaw elektrotechniki", Wyd. PP, Poznań, 2000

4. Frąckowiak J., Nawrowski R., Zielińska M. "Podstawy elektrotechniki", Laboratorium, Wyd. PP, Poznań, 2011

Result of average student's workload

Activity	Time (working hours)
1. participation in lectures	10
2. participation in tutorial classes	10
3. participation in consulting (lecture)	8
4. participation in consulting (tutorial)	7
5. preparation to test/exam	20
6. test/exam	2
7. preparation for exercise classes, drawing homeworks up	14

Student's workload

Source of workload	hours	ECTS
Total workload	71	3
Contact hours	37	1
Practical activities	0	0